

Substitute for Form 1449/PTO  <b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>  Page 1 of 2	<b>Application No.</b>	10/613,776
	<b>Filed</b>	7/2/2003
	<b>First Inventor</b>	Srinivasan, Varadarajan
	<b>Art Unit</b>	2661
	<b>Examiner</b>	
	<b>Atty. Docket No.</b>	NLMI.P213

Non Patent Literature Documents		
Examiner Initials	Name of Author, Title of Item, Date, Page(s), Volume-Issue Number(s), Publisher, City and/or Country where Published	Trans- lation
	Efficient Fair Queuing Using Deficit Round Robin, M. Shreedhar, George Varghese, (Specific date of publication unknown -- at least prior to 7/2/2003)	
	genFlow OC-48c Multiprotocol Traffic Management Coprocessor, Acorn Networks, (Specific date of publication unknown -- at least prior to 7/2/2003)	
	genFlow OC-48c CAN2500gF OC48c Multiprotocol Traffic Management Coprocessor, Acorn Networks, (Specific date of publication unknown -- at least prior to 7/2/2003)	
	Hierarchical Packet Fair Queueing Algorithms, Jon C.R. Bennett, Hui Zhang, (Specific date of publication unknown -- at least prior to 7/2/2003)	
	Traffic Stream Processor MXT 4400, Conexant, (Specific date of publication unknown -- at least prior to 7/2/2003)	

<b>Examiner Signature</b>		<b>Date Considered</b>	
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\*Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw a line through citation if not in conformance and not considered. Include a copy of this form with next communication to applicant.

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	WF2Q: Worst-case Fair Weighted Fair Queueing, Jon C.R. Bennett, Hui Zhang. (Specific date of publication unknown -- at least prior to 7/2/2003)	
	What is a Network Processor?, Vitesse Semiconductor Corp., (Specific date of publication unknown -- at least prior to 7/2/2003)	

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